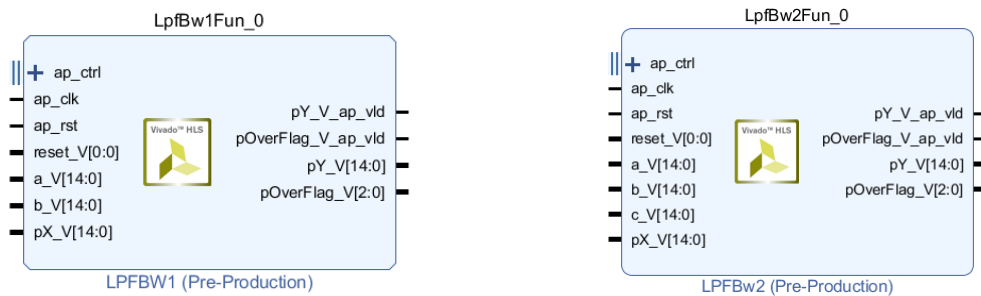


Digital Filter Modules

By Dr. Qielu Pan Nov. 2020

Dr. Pan has developed a set of digital signal filters independently based on Xilinx FPGA, including first-order LPF, second-order LPF, BSP, BPF, etc. We can provide a time-limited version and related initial program for your test.



The main interface parameters of the module are as follows:

No.	Items	Value
1	Input clock	100MHz
2	Filter coefficients (a, b, c...)	15 bits
3	Input signal (pX_V)	15 bits
4	Output signal (pY_V)	15 bits

Test results: (Band Stop Filter--BSF)

