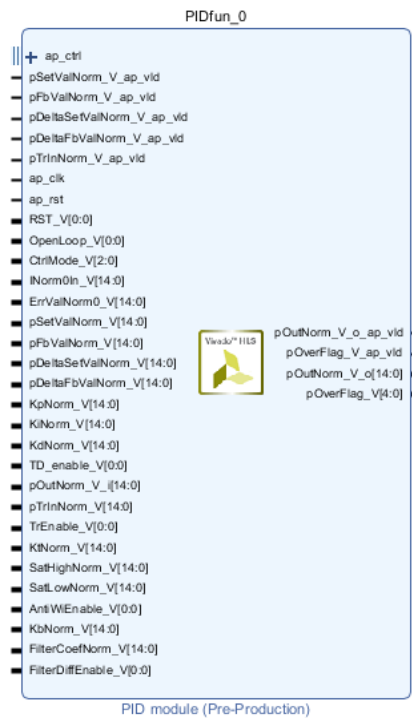# PID Controller Modules

By Dr. Qielu Pan   Oct. 2020

Dr. Pan has developed the PID controller independently based on Xilinx FPGA. It has the anti-windup function and amplitude-limit function. User can select the control mode and signal input mode freely. The control type includes P, PI, PID, PD. We can provide customized version according to your requirements and related initial program for your test.

The main interface parameters of the module are as follows:

No.	Items	Value
1	Input clock	100MHz
2	PID coefficients	15 bits
3	Error signal	15 bits
4	Setting signal	15 bits
5	Feedback signal	15 bits
6	Output signal	15 bits