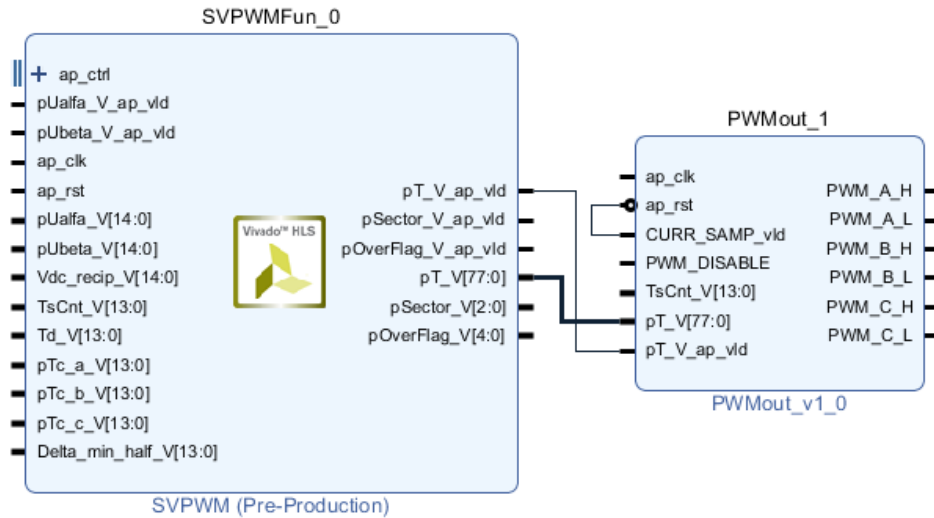


# SVPWM Module

By Dr. Qielu Pan September, 2021

Dr. Pan developed this SVPWM module independently based on Xilinx FPGA. It can be used in PMSM FOC drive. It includes two parts: SVPWM block and PWMout block. We can provide a time-limited version and related initial program for your test.



The main input parameters of the module are as follows:

No.	Items	Description
1	U_alfa	15bits, voltage alfa
2	U_beta	15bits, voltage beta
3	TsCnt_V	14bits PWM frequency counter
4	pTc_a_V, pTc_b_V, pTc_c_V	14bits, compensation of dead zone
5	Vdc_recip_V	DC Bus voltage
6	Delta_min_half_V	Minimum narrow PWM pulse width.

Test results:

